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Vaughan-Nichols, S.J.;
Computer ,Volume: 37 , Issue: 2 , Feb. 2004
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[Abstract] [PDF Full-Text (350KB)] **IEEE JNL**

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Vaughan-Nichols, S.J.;
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[Abstract] [PDF Full-Text (333KB)] **IEEE JNL**

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Vaughan-Nichols, S.J.;
Computer ,Volume: 36 , Issue: 8 , Aug. 2003
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6 **The algebra of λ matrices: An effective tool for sensitivity analysis**

Atherton, D.; Dorrah, H.; Nichols, S.;
Automatic Control, IEEE Transactions on ,Volume: 21 , Issue: 6 , Dec 1976
Pages:881 - 882

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7 **Technology news - Statistical language approach translates into su**
Vaughan-Nichols, S.J.;
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8 **Building better software with better tools**
Vaughan-Nichols, S.J.;
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[Abstract] [PDF Full-Text (411KB)] IEEE JNL

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9 **Saving private e-mail**
Vaughan-Nichols, S.J.;
Spectrum, IEEE ,Volume: 40 , Issue: 8 , Aug. 2003
Pages:40 - 44

[Abstract] [PDF Full-Text (1013KB)] [Full-Text HTML] IEEE JNL

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10 **The challenge of Wi-Fi roaming**
Vaughan-Nichols, S.J.;
Computer ,Volume: 36 , Issue: 7 , July 2003
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[Abstract] [PDF Full-Text (343KB)] IEEE JNL

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11 **OSs battle in the smart-phone market**
Vaughan-Nichols, S.J.;
Computer ,Volume: 36 , Issue: 6 , June 2003
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[Abstract] [PDF Full-Text (371KB)] IEEE JNL

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12 **Engineering Entrepreneurship: does entrepreneurship have a role i engineering education?**
Nichols, S.P.; Armstrong, N.E.;
Antennas and Propagation Magazine, IEEE ,Volume: 45 , Issue: 1 , Feb. 200
Pages:134 - 138

[Abstract] [PDF Full-Text (390KB)] IEEE JNL

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13 **XML raises concerns as it gains prominence**
Vaughan-Nichols, S.J.;
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[Abstract] [PDF Full-Text (293KB)] IEEE JNL

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14 **The battle over the universal Java IDE**
Vaughan-Nichols, S.J.;
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15 **Mobile IPv6 and the future of wireless Internet access**
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Computer ,Volume: 36 , Issue: 2 , Feb. 2003
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1 Formal specification and validation at work: a case study using VDM-SL 87%
Sten Agerholm , Pierre-Jean Lecoeur , Etienne Reichert
Proceedings of the second workshop on Formal methods in software practice March 1998

2 OBSERV---a prototyping language and environment 85%
Shmuel Tyszberowicz , Amiram Yehudai
ACM Transactions on Software Engineering and Methodology (TOSEM) July 1992 Volume 1 Issue 3

The OBSERV methodology for software development is based on rapid construction of an executable specification, or prototype, of a systems, which may be examined and modified repeatedly to achieve the desired functionality. The objectives of OBSERV also include facilitating a smooth transition to a target system, and providing means for reusing specification, design, and code of systems and subsystems. We are particularly interested in handling embedded systems, which are likely to have conc ...

3 An adversarial plan recognition system for multi-agent airborne threats 84%
Richard D. Amori
Proceedings of the 1992 ACM/SIGAPP Symposium on Applied computing: technological challenges of the 1990's April 1992

4 Interactive graphics and menus for computer network models 82%
Donald F. DuBois
Proceedings of the 15th conference on Winter Simulation - Volume 2 December 1983

The Hierarchical Modeling System (HMS) is a single software package that provides a

discrete event simulation and analytic modeling capability for computer networks. The analyst may reconfigure the network model depending on the number of nodes and their interconnections by making a few changes to some simple data files-no recoding of the model is necessary. This paper describes an interactive graphics front end for HMS which allows the analyst the capability for reconfiguring the model at ...

5 Yesterday, my program worked. Today, it does not. Why? 82%

Andreas Zeller

ACM SIGSOFT Software Engineering Notes , Proceedings of the 7th European engineering conference held jointly with the 7th ACM SIGSOFT international symposium on Foundations of software engineering October 1999

Volume 24 Issue 6

Imagine some program and a number of changes. If none of these changes is applied ("yesterday"), the program works. If all changes are applied ("today"), the program does not work. Which change is responsible for the failure? We present an efficient algorithm that determines the minimal set of failure-inducing changes. Our delta debugging prototype tracked down a single failure-inducing change from 178,000 changed GDB lines within a few hours.

6 Introducing formal specification methods in industrial practice 82%

Luciano Baresi , Alessandro Orso , Mauro Pezzè

Proceedings of the 19th international conference on Software engineering May 1997

7 DDD—a free graphical front-end for UNIX debuggers 82%

Andreas Zeller , Dorothea Lütkehaus

ACM SIGPLAN Notices January 1996

Volume 31 Issue 1

The Data Display Debugger (DDD) is a novel graphical user interface to GDB and DBX, the popular UNIX debuggers. Besides "usual" features such as viewing source texts and breakpoints, DDD provides a *graphical data display*, where data structures are displayed as graphs. A simple mouse click dereferences pointers or reveals structure contents. Complex data structures can be explored incrementally and interactively, using automatic layout if preferred. Each time the program stops, the data di ...

8 Using the web for document versioning: an implementation report for DeltaV 80%

James J. Hunt , Jürgen Reuter

Proceedings of the 23rd international conference on Software engineering July 2001

The current suite of systems that offer client/server capabilities for document versioning relies on proprietary protocols for communicating between a central versioning repository and a remote client. In order to support better document authoring via the Web, the DeltaV working group of the Web-DAV (WWW Distributed Authoring and Versioning) project of the Internet Engineering Task Force is working on a standard protocol for versioning over HTTP. The authors present a prototype of DeltaV b ...

9 OdeView: the graphical interface to Ode 80%

R. Agrawal , N. H. Gehani , J. Srinivasan

ACM SIGMOD Record , Proceedings of the 1990 ACM SIGMOD international conference on Management of data May 1990
Volume 19 Issue 2

OdeView is the graphical front end for Ode, an object-oriented database system and environment. Ode's data model supports data encapsulation, type inheritance, and complex objects. OdeView provides facilities for examining the database schema (i.e., the object type or class hierarchy), examining class definitions, browsing objects, following chains of references starting from an object, synchronized browsing, displaying selected portions of objects (projection), and retrieving objects with ...

10 Power and energy: A hierarchical approach for energy efficient application design using heterogeneous embedded systems 80%

Sumit Mohanty , Viktor K. Prasanna
Proceedings of the international conference on Compilers, architectures and synthesis for embedded systems October 2003

Several features such as reconfiguration, voltage and frequency scaling, low-power operating states, duty-cycling, etc. are exploited for latency and energy efficient application design using heterogeneous embedded systems. However, more choices during application design results in a large design space that must be traversed efficiently. In this paper, we propose a hierarchical methodology that integrates optimization heuristics, high-level performance estimators, and low-level simulators to ena ...

11 Internet-Based Collaborative Test Generation with MOSCITO 80%

A. Schneider , K. Diener , E. Ivask , J. Raik , R. Ubar , P. Miklos , T. Cibáková , E. Gramatová
Proceedings of the conference on Design, automation and test in Europe March 2002

This paper offers an Internet-based environment forenhancing problem-specific design flows with test patterngeneration and fault simulation capabilities. AutomaticTest Pattern Generation (ATPG) and fault simulation toolsat structural and hierarchical levels available at geographicallydifferent places running under the virtual environmentusing the MOSCITO system are presented. Thesetools can be used separately, or in multiple applications,for test pattern generation of digital circuits. In order ...

12 A visual representation for knowledge structures 80%

M. Travers
Proceedings of the second annual ACM conference on Hypertext November 1989

Knowledge-based systems often represent their knowledge as a network of interrelated units. Such networks are commonly presented to the user as a diagram of nodes connected by lines. These diagrams have provided a powerful visual metaphor for knowledge representation. However, their complexity can easily become unmanageable as the knowledge base (KB) grows. This paper describes an alternate visual representation for navigating knowledge structures, based on a virtual museum metap ...

13 The KScalar simulator 80%

J. C. Moure , Dolores I. Rexachs , Emilio Luque
Journal on Educational Resources in Computing (JERIC) March 2002
Volume 2 Issue 1

Modern processors increase their performance with complex microarchitectural mechanisms, which makes them more and more difficult to understand and evaluate.

KScalar is a graphical simulation tool that facilitates the study of such processors. It allows students to analyze the performance behavior of a wide range of processor microarchitectures: from a very simple in-order, scalar pipeline, to a detailed out-of-order, superscalar pipeline with non-blocking caches, speculative execution, and comp ...

14 Does it make a difference if students exercise on the web or in the classroom? 80%

Ari Korhonen , Lauri Malmi , Pertti Myllyselkä , Patrik Scheinin

ACM SIGCSE Bulletin , Proceedings of the 7th annual conference on Innovation and technology in computer science education June 2002

Volume 34 Issue 3

Several Web-based learning environments which can automatically give immediate feedback to the students have been reported within the past few years. The quality of feedback can be relatively high in these systems, but it does not achieve the level a trained teacher can provide. However, the lack of the best possible feedback can be compensated for, to some extent, by providing immediate and individualised feedback at any place or time. The question is whether the systems providing automatic fee ...

15 Advanced notations in mathematica 80%

Jason Harris

Proceedings of the 2000 international symposium on Symbolic and algebraic computation symbolic and algebraic computation July 2000

This paper outlines the functionality and implementation of the Notation package, an extension to the Mathematica front end, allowing the user to introduce advanced notations. Following this, several advanced example notations are presented. These include complete and functioning notations for both Dirac's bra-ket notation as well as for tensorial expressions. Proper functioning notations for both of these objects have not been previously presented in a maj ...

16 System Administration: The Linux Trace Toolkit 80%

Karim Yaghmour , Michel Dagenais

Linux Journal May 2000

Analyzing performance is one of the most important tasks of a system administrator; here's how to do it using Linux.

17 Constraints in constructive solid geometry 80%

Jaroslaw R. Rossignac

Proceedings of the 1986 workshop on Interactive 3D graphics January 1987

The success of solid modelling in industrial design depends on facilities for specifying and editing parameterized models of solids through user-friendly interaction with a graphical front-end. Systems based on a dual representation, which combines Constructive Solid Geometry (CSG) and Boundary representation (BRep), seem most suitable for modelling mechanical parts. Typically they accept a CSG-compatible input (Boolean combinations of solid primitives) and offer facilities for parameterizi ...

18 Robert Young of Red Hat Software 80%

Marjorie Richardson

Linux Journal August 1999

80%

19 Design components: toward software composition at the design level

Rudolf K. Keller , Reinhard Schauer
Proceedings of the 20th international conference on Software engineering April 1998

20 The challenge of the Internet explosion: ideas for staying ahead of the users 80%

Audun S. Runde
Proceedings of the 21st annual ACM SIGUCCS conference on User services November 1993

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Volume 24 Issue 6
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2 DDD—a free graphical front-end for UNIX debuggers 82%
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ACM SIGPLAN Notices January 1996
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3 Formal specification and validation at work: a case study using VDM-SL 80%
Sten Agerholm , Pierre-Jean Lecoeur , Etienne Reichert
Proceedings of the second workshop on Formal methods in software practice
March 1998

4 Query-based debugging of object-oriented programs 80%

Raimondas Lencevicius , Urs Hölzle , Ambuj K. Singh

ACM SIGPLAN Notices , Proceedings of the 12th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications October 1997

Volume 32 Issue 10

Object relationships in modem software systems are becoming increasingly numerous and complex. Programmers who try to find violations of such relationships need new tools that allow them to explore objects in a large system more efficiently. Many existing debuggers present only a low-level, one-object-at-a-time view of objects and their relationships. We propose a new solution to overcome these problems: query-based debugging. The implementation of the query-based debugger described here offers ...

5 Generating wrappers for command line programs: the Cal-Aggie Wrap-O-Matic project 77%

Eric Wohlstadter , Stoney Jackson , Premkumar Devanbu

Proceedings of the 23rd international conference on Software engineering July 2001

Software developers writing new software have strong incentives to make their products compliant to standards such as CORBA, COM, *and Java Beans. Standards-compliance facilitates inter-operability, component-based software assembly, and software reuse, thus leading to improved quality and productivity. Legacy software, on the other hand, is usually monolithic, and hard to maintain and adapt. Many organizations, saddled with entrenched legacy software, are confronted with the need to ...*

6 Network processors: a perspective on market requirements, processor architectures and embedded S/W tools 77%

P. Paulin , F. Karim , P. Bromley

Proceedings of the conference on Design, automation and test in Europe March 2001

7 Session 5: university education: Distributed exploratorium for high performance computational techniques 77%

Sridhar V. Iyer , Alan Beck , Umberto Ravaioli , Jeff Terstriep

Proceedings of the 1994 ACM/IEEE conference on Supercomputing November 1994

The exploratorium described here consists of hyperlinked tutorials and quizzes on high performance computational concepts and computational modules implemented on different computer systems: desktop workstations, vector and parallel supercomputers. The exploratorium provides a highly interactive environment in which students can experiment with computational modules and visually compare algorithms, numerical techniques, performance and implementation related issues. Such a hands-on simulation an ...

8 The KScalar simulator 77%

J. C. Moure , Dolores I. Rexachs , Emilio Luque

Journal on Educational Resources in Computing (JERIC) March 2002

Volume 2 Issue 1

Modern processors increase their performance with complex microarchitectural

mechanisms, which makes them more and more difficult to understand and evaluate. KScalar is a graphical simulation tool that facilitates the study of such processors. It allows students to analyze the performance behavior of a wide range of processor microarchitectures: from a very simple in-order, scalar pipeline, to a detailed out-of-order, superscalar pipeline with non-blocking caches, speculative execution, and comp ...

9 Review: KDevelop 1.4 77%
Petr Sorfa
Linux Journal July 2001
Volume 2001 Issue 87

10 The design and implementation of the open Ravenscar kernel 77%
Juan A. de la Puente , Juan Zamorano , José Ruiz , Ramón Fernández , Rodrigo García
ACM SIGAda Ada Letters , Proceedings of the 10th international workshop on Real-time Ada workshop March 2001
Volume XXI Issue 1
This paper describes the design and implementation of Open Ravenscar Kernel (ORK), an open-source real-time kernel of reduced size and complexity, for which users can seek certification for mission-critical space applications. The kernel supports Ada 95 tasking on an ERC32 (SPARC v7) architecture in an efficient and compact way. It is closely integrated with the GNAT runtime library and other tools.

11 A generic tool set for application specific processor architectures 77%
Frank Engel , Johannes Nührenberg , Gerhard P. Fettweis
Proceedings of the eighth international workshop on Hardware/software codesign May 2000
Retargetability allows an easy adoption of a simulator on different processor architectures without a time consuming redesign of all tools. This is evident for an efficient HW/SW codesign.In this paper we describe a tool set for fast and easy simulation of processor architectures based on a retargetable simulator core. This approach helps to reduce the development time for designing and validating System-on-a-chip (SoC) applications based on a processor core. The use of ANSIC avo ...

12 CodeWarrior for Red Hat, Linux, GNU Edition, Version 4 77%
Jason Kroll
Linux Journal October 1999

13 Porting DOS Applications to Linux: Lots of practical tips for porting your DOS applications 77%
Alan Cox
Linux Journal September 1995

14 Moving industry-guided multimedia technology into the classroom 77%
P. K. McKinley , B. H. C. Cheng , J. J. Weng
ACM SIGCSE Bulletin , The proceedings of the thirtieth SIGCSE technical symposium on Computer science education March 1999
Volume 31 Issue 1
Given the ubiquity of multimedia technology, it is important that Computer Science students not only learn the basics of multimedia design, but also gain hands-on experience with applications of the technology. This paper describes the integration of

multimedia concepts and tools into a Computer Science curriculum. An NSF-sponsored Multimedia Laboratory was established and used to support three senior-level courses: software engineering, computer graphics, and computer networks. Curriculum devel ...

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